Docket No.: 08211/0200387-US0/P05824

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: David J. Megaw et al.

Application No.: 10/801,219

Confirmation No.:

Filed: 3/16/04

Art Unit: N/A

For:

APPARATUS AND METHOD FOR

CALIBRATING A BANDGAP REFERENCE

VOLTAGE

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed within three months of the U.S. filing date (37 CFR 1.97(b)(1)).

A copy of each document, except U.S. patents, on the PTO/SB/08 is attached.

The Commissioner is authorized to charge any deficiency of up to \$300.00 or credit any excess in this fee to Deposit Account No. 04-0100.

Dated: April 7, 2004

Respectfully submitted,

Matthew M. Gaffney

Registration No.: 46, \$\frac{1}{2}\$7

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OC. W.		Application Number	10/801,219
TRANSMITTAL		Filing Date	3/16/2004
FORM		First Named Inventor	David J. Megaw
(to be used for all correspondence after initial filing)		Art Unit	N/A
		Examiner Name	Not Yet Assigned
Number of Pages in This Submission	1	Attorney Docket Number	08211/0200387-US0/P05824

					
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Fee	Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences		
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	SIGNAT	URE OF APPLICANT, ATTORNEY, OF	RAGENT		
Firm or Individual name	DARBY & DARBY P.C. Matthew M. Gaffney - 46,717				
Signature	Matt Goffely				
Date	April 7, 2004				

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Attorney Docket No.: 08211/0200387-US0

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number

10/801,219

Filing Date

3/16/2004

First Named Inventor

Art Unit

N/A

Examiner Name

National Semiconductor

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			3 U.S. P.	ATENT DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US-6,342,781	01/29/02	Laraia	
	AB	US-4,808,908	02/28/1989	Lewis et al.	
	AC	US-4 443 753	04/17/1984	McGlinchev	

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Examiner	Cite	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines,	
Initials*	No.1	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Date MM-DD-YYYY	Vynere Relevant Passa	Where Relevant Passages or Relevant Figures Appear	T⁵

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		6 NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No. ¹	and/or country where published.	T²
	CA	AUDY, JONATHAN M. 3 rd order curvature corrected Bandgap Cell. IEEE, 1996, 397-400.	
	СВ	AZARKAN, AHMIDOU, ARIE VAN STAVEREN and FABIANO FRUETT. A Low-noise Bandgap Reference Voltage Source with Curvature Correction. IEEE, 2002, 205-208.	
	CC	GUNAWAN, MADE ET AL. A Curvature-Corrected Low-Voltage Bandgap Reference. IEEE, 1993, 667670.	
	CD	GUPTA, SANDHYA and WILLIAM BLACK. A 3 to 5V CMOS Bandgap Voltage Reference with Novel Trimming. IEEE, 1997, 969-972.	
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	CF	MICHEJDA, JOHN and SUK K. KIM. A Precision CMOS Bandgap Reference. IEEE Journal of Solid-State Circuits, December 1984, Vol. Sc 19:6: 1014-1021	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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